

# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER OF PATENTS AND TRADEMARKS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FI	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/611,955	(	07/06/2000	Cyprian Emeka Uzoh	FI9-97-205B	6678
32074	7590	05/29/2003			
INTERNATIONAL BUSINESS MACHINES CORPORATION DEPT. 18G BLDG. 300-482				EXAMINER	
				VU, HUNG K	
2070 ROUT		ON, NY 12533		ART UNIT	PAPER NUMBER
HOI E WEEL	3 3 5 7 1 6 7 7	011,111 12000		2811	
	DATE MAILEI			DATE MAILED: 05/29/2003	3

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
	09/611,955	UZOH ET AL.					
Office Action Summary	Examiner	Art Unit					
	Hung K. Vu	2811					
The MAILING DATE of this communication app Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).  Status	i6(a). In no event, however, may a reply be within the statutory minimum of thirty (30) of ill apply and will expire SIX (6) MONTHS for cause the application to become ABANDO	timely filed lays will be considered timely. om the mailing date of this communication. NED (35 U.S.C. § 133).					
1) Responsive to communication(s) filed on $\underline{23}  \underline{\mathcal{L}}$	December 2002 .						
, <del></del> .	s action is non-final.						
3) Since this application is in condition for allowa	This design is the second section of the morits is						
Disposition of Claims	Ex parte Quaylo, 1000 0.5. 11	, 100 0101 2131					
4)⊠ Claim(s) <u>25-32</u> is/are pending in the applicatio	n.						
4a) Of the above claim(s) is/are withdraw							
5) Claim(s) is/are allowed.							
6)⊠ Claim(s) <u>23-32</u> is/are rejected.							
7) Claim(s) is/are objected to.							
8) Claim(s) are subject to restriction and/or	r election requirement.						
Application Papers							
9) ☐ The specification is objected to by the Examine							
10) ☐ The drawing(s) filed on is/are: a) ☐ accept							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
11) ☐ The proposed drawing correction filed on is: a) ☐ approved b) ☐ disapproved by the Examiner.							
If approved, corrected drawings are required in rep							
12) The oath or declaration is objected to by the Ex	ammer.						
Priority under 35 U.S.C. §§ 119 and 120		0(2) (4) 22 (8					
13) Acknowledgment is made of a claim for foreign	n priority under 35 U.S.C. § 11	⊌(a)-(a) or (i).					
a) ☐ All b) ☐ Some * c) ☐ None of:							
1. Certified copies of the priority document		antino No					
2. Certified copies of the priority document							
<ul> <li>3. Copies of the certified copies of the prio application from the International Bu</li> <li>* See the attached detailed Office action for a list</li> </ul>	ireau (PCT Rule 17.2(a)).						
14) ☐ Acknowledgment is made of a claim for domesti			).				
a) ☐ The translation of the foreign language pro	ovisional application has been	received.					
Attachment(s)							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Inform	nary (PTO-413) Paper No(s) nal Patent Application (PTO-152)					

Art Unit: 2811

### **DETAILED ACTION**

### **Drawings**

1. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the electrical insulating layer located over at least one major surface and in recesses, as recited in claim 25, must be shown or the feature(s) canceled from the claim(s). Note that Figure 3 shows recesses 2 located in the insulating layer 3, not an insulating layer 3 located in the recesses 2. No new matter should be entered.

A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

### Specification

2. The amendment filed 12/23/02 is objected to under 35 U.S.C. 132 because it introduces new matter into the disclosure. 35 U.S.C. 132 states that no amendment shall introduce new matter into the disclosure of the invention. The added material which is not supported by the original disclosure is as follows: recesses 2 are formed <u>in</u> at least one major surface of the semiconductor substrate.

Applicant is required to cancel the new matter in the reply to this Office Action.

#### Claim Objections

3. Claims 25 and 31-32 are objected to because of the following informalities:

Art Unit: 2811

In claim 25, line 2, "in" should be changed to "on" for clarity. Note that the specification, at page 7, lines 16-18, and page 13, lines 2-4, only describes recesses located on at least one major surface of the semiconductor substrate.

Appropriate correction is required.

# Claim Rejections - 35 USC § 112

4. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 25-32 are rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.

The limitations ("a conductive barrier located over said insulating layer in said recess and over said at least one major surface" and "a conductive metal in said recess only"), as recited in claim 25, are not supported by the original specification and/or drawings. Note that Figure 3 shows the conductive barrier (4) located over the insulating layer (3) in recess and over at least one major surface, but does not show the conductive metal (8) is in recess only. Figure 4, on the other hand, shows the conductive metal (8) in recess only and the conductive barrier (4) located over the insulating layer (3) in recess, but does not show the conductive barrier (4) located over at least one major surface. Also note that Figure 3 is an intermediate structure, while Figure 4 is a final structure.

Art Unit: 2811

# Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 25, 28-29 and 31-32, insofar as in compliance with 35 USC 112, are rejected under 35 U.S.C. 102(e) as being anticipated by Jain (PN 5,821,168, of record).

Jain discloses, as shown in Figure 10, a semiconductor structure comprising,

a semiconductor substrate (12);

recess (72) located on at least one major surface of the semiconductor substrate; electrical insulating layer (56) located over the at least one major surface and in the recesses;

a conductive barrier (not shown, Col. 5, lines 38-43) located over the insulating layer in the recess;

a seed layer (60 then became part of 74) located over the conductive barrier within the recess only;

a conductive metal (62 then became part of 74) in the recess only. Note that at the final structure, the seed layer and the conductive metal layer (74) is in recess only.

With regard to claim 28, Jain discloses the seed layer is copper (Col. 4, line 14).

Art Unit: 2811

With regard to claim 29, the term "sputtered" is method recitation in a device claimed, and it is non-limiting, because only the final product is relevant, not the method of making. A product by process claim is directed to the product per se, no matter how actually made. See also MPEP 2113. Moreover, an old or obvious product produced by a new method is not a patentable product, whether claimed in "product by process" claims or not.

With regard to claim 31, Jain discloses the conductive metal is copper (Col. 4, lines 28-29).

With regard to claim 32, Jain discloses the conductive metal is about 6,000Å to 15,000Å thick (within the range of 4,000Å to 30,000Å thick) (Col. 4, lines 28-29).

### Response to Arguments

6. Applicant's arguments filed 10/19/01 have been fully considered but they are not persuasive.

It is argued, at page 5 of the Remarks, that Figure 3 shows a recess 2 in at least one major surface of a substrate 13, and insulating layer 3 located over the major surface and in the recess 2. This argument is not convincing because Figure 3 only shows recess 2 on at least one major surface of a substrate 13, insulating layer 3 located over the major surface of the substrate, and the recess 2 is in the insulating layer, <u>not</u> insulating layer 3 in the recess 2.

Art Unit: 2811

It is argued, at page 6 of the Remarks, that Figure 7 of Jain does not disclose the seed layer is located only in recess and a conductive metal 8 in the recess 2 only. This argument is not convincing due to above USC 112, first paragraph. Further Jain discloses, as shown in Figure 10, the final structure whereby the seed layer and copper layer (74) is formed in recess, therefore, it is inherent that the seed layer is located in recess only. Note that Figure 3 of the claimed invention shows the conductive barrier (4) located over the insulating layer (3) in recess and over at least one major surface, but does not show the conductive metal (8) is in recess only. Figure 4 of the claimed invention, on the other hand, shows the conductive metal (8) in recess only and the conductive barrier (4) located over the insulating layer (3) in recess, but does not show the conductive barrier (4) located over at least one major surface. Also note that Figure 3 is an intermediate structure, while Figure 4 is a final structure.

#### Conclusion

7. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event,

Art Unit: 2811

however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hung K. Vu whose telephone number is (703) 308-4079. The examiner can normally be reached on Mon-Thurs 7:00-4:30, alternate Friday 7:00-3:30, Eastern Time.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (703) 308-2772. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Vu

May 19, 2003

Stoven Loke
Primary Examinor

Steven Loke